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		FILING DATE: July 30, 2001		CONFIRMATION NO.: 4927	
		APPLICANT: Sebastien Ferroussat			
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Sheet		of			

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
MM		4,604,723		James L. Burrows	08/05/86

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	
MM		European Search Report from European Patent Application 00410092.1 filed August 1, 2000		
↓		Mayur Mehta et al. "High-Speed Multiplier Design Using Multi-Input Counter And Compressor Circuits" Proceedings of the Symposium on Computer Arithmetic, US, Los Alamitos, IEEE Comp. Soc. Press, vol. SYMP. 10, June 26, 1991, pages 43-50, XP00022157		
✓		Norio Ohkubo et al. "A 4.4 NS CMOS 54 X 54-B Multiplier Using Pass-Transistor Multiplexer" IEEE Journal of Solid-State Circuits, US, IEEE Inc., New York, vol. 30, no. 3, March 1, 1995, pages 251-256, XP000502811		

EXAMINER MM	DATE CONSIDERED 3/13/06
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